

# **SR176**

# 13.56MHz Short Range Contactless Memory Chip 176 bit USER EEPROM and 64 bits Unique ID

**BRIEF DATA** 

- ISO 14443 2 Type B Air Interface Compliant
- ISO 14443 3 Type B Frame Format Compliant
- 13.56MHz Carrier Frequency
- 847kHz Sub-carrier Frequency
- 106K bit/s Data Transfer
- Data Transfert
  - ASK Modulation from Reader to tag
  - BPSK Coding from tag to Reader
- 64 bits Unique Identifier
- 176 bit EEPROM with Write Protect Feature
- READ BLOCK & WRITE BLOCK (16 Bits)
- Internal Tuning Capacitor
- 100K ERASE/WRITE Cycles
- 10 Years Data Retention
- Self-Timed Programming Cycle
- 5ms Typical Programming Time

## **DESCRIPTION**

The SR176 is a contactless memory powered by the received carrier electromagnetic wave. It is a 176 bits user EEPROM fabricated with STMicroelectronics CMOS technology. The memory is organised as 16 blocks of 16 bits on which 11 blocks are user accessible. The SR176 is accessed via the 13.56MHz carrier. Incoming data are demodulated and decoded from the received Amplitude Shift Keying modulation signal (ASK) and outgoing data are generated by load variation using Bit Phase Shift Keying coding (BPSK) of a 847kHz sub-carrier. The received ASK wave is 10% modulated. The Data transfer rate between the SR176 and the reader is 106Kbit/s in both reception and emission modes

The SR176 follows the ISO 14443 part 2 type B recommendation for Radio frequency power and signal interface.

Figure 1. Delivery Forms

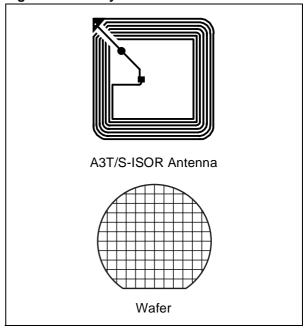
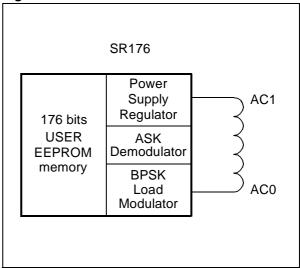


Figure 2. Pad Connections



**Table 1. Signal Names** 

AC1	Antenna Coil
AC0	Antenna Coil

May 2002 1/3

### **DESCRIPTION** (cont'd)

The SR176 is principally designed for short range applications, such as in object identification, that need a low cost and non-reusable product. The SR176 does not include any anti-collision mechanism. It provides an "addressed" selection mechanism to cope with cases where more than one tag is present within the range of the reader.

The SR176 contactless EEPROM memory offers read and write random access in block mode. One block is composed by 16 bits. The device has an instruction set containing 7 commands:

- READ BLOCK
- WRITE BLOCK
- INITIATE
- SELECT
- **■** COMPLETION
- PROTECT BLOCK
- GET\_PROTECTION.

The SR176 is divided into two major areas: the unique identifier (UID) and the User EEPROM. The UID is a 64 bits unique identifier written by ST during product manufacturing. The User EEPROM is divided into areas which can be write protected in

order to behaves as ROM. The write protection is activated using an OTP lock bits register.

It is possible to program the SR176 four bits chip\_ID used by the SELECT command. Its default value is fixed at the value 15 (1111<sub>b</sub>) by ST. When correctly set, up to 15 SR176 can be selected individually.

#### **MEMORY MAPPING**

The SR176 is organised as 16 blocks of 16bits as shown in Figure 3.

The first 4 blocks, from location 0 to 3, are used to store read only data. They store the 64 bits UID. This value cannot be modified.

Blocks from location 4 to 14 offer a 176 bits EEP-ROM user area on which the application will store its data values. Blocks 15 containt the OTP lock bits and the programmed Chip\_ID.

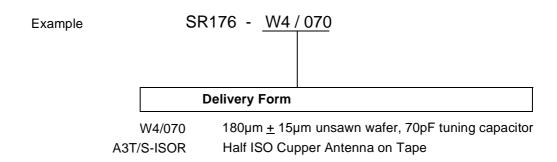
The PROTECT\_BLOCK command is used to lock write access to blocks 4 to 15 by group of 2 blocks.

The GET\_PROTECTION command gives the status of the protection of blocks 4 to 15.

Figure 3. SR176 Memory Mapping

Block Address	Msb b <sub>15</sub>			Description		
0						
1	64 bits UID				ROM	
2						
3						
4		User	EEPROM Lockable			
5		User				
6		User	EEPROM Lockable			
7		User				
8		User	EEPROM Lockable			
9		User				
10	User Area				EEPROM Lockable	
11		User	LEI KOW LOCKADIE			
12		User	EEPROM Lockable			
13		User				
14		User	EEPROM Lockable			
15	OTP Id	ock bits	Reserved	Chip_ID	EEFNOW LOCKABLE	

#### ORDERING INFORMATION SCHEME



For a full list of the available options, please see the current Memory Shortform Catalogue. For further information on any aspect of this device, please contact your nearest ST Sales Office.

E-mail contact: memories.contactless@st.com

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is registered trademark of STMicroelectronics All other names are the property of their respective owners

© 2002 STMicroelectronics - All Rights Reserved

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States

www.st.com



This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.